## Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | HAYAKAWA ET AL. | Examiner | John M Ross | 2188 | Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			
	ပ	US-			
	О	US-			
	Е	US-			
	F	US-			
	G	US-			
	Ι	US-			
	ı	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q				,	
	R					
	S					
	T					

## **NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	David H. Albonesi, "Selective Cache Ways: On-Demand Cache Resource Allocation," Proceedings of the 32nd annual ACM/IEEE international symposium on Microarchitecture, 1999, pp 248-259.				
	V					
	w	·				
	x					

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.